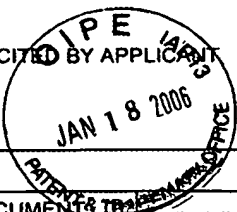


Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 251023US2S		SERIAL NO. 10/807,454	
LIST OF REFERENCES CITED BY APPLICANT <div style="text-align: center; margin-top: 20px;">  </div>				APPLICANT Yoshihisa IWATA et al.			
				ALLOWED DATE December 6, 2005	FILING DATE March 24, 2004	GROUP 2827	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
<b>FOREIGN PATENT DOCUMENTS</b>							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
TP	AO	2003-173672	06/20/2003	JAPAN		X	
TP	AP	2003-257175	09/12/2003	JAPAN		X	
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
<b>OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
TP	AW	Takeshi HONDA et al., "MRAM-Writing Circuitry to Compensate for Thermal-Variation of Magnetization-Reversal Current", 2002 Symposium On VLSI Circuits Digest of Technical Papers, IEEE, June 2002, pp. 156-157.					
	AX						
	AY						
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner <i>Trong Phan</i>					Date Considered <i>1/31/06</i>		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							